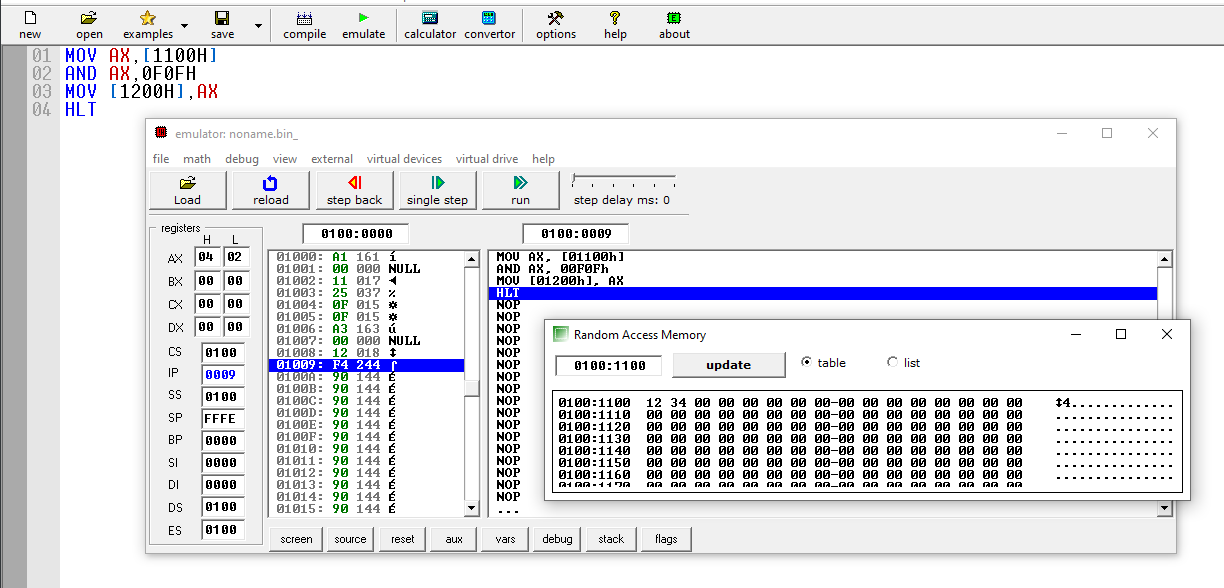
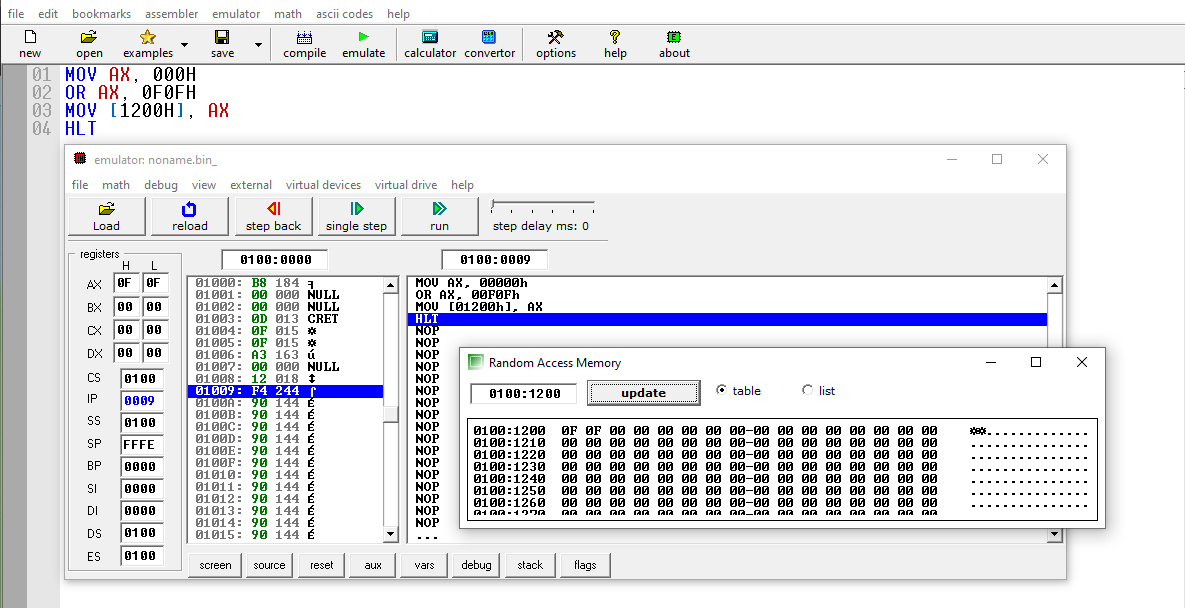
**Microprocessor, Microcontroller and Interface Techniques Lab**

**AND Operation Output**

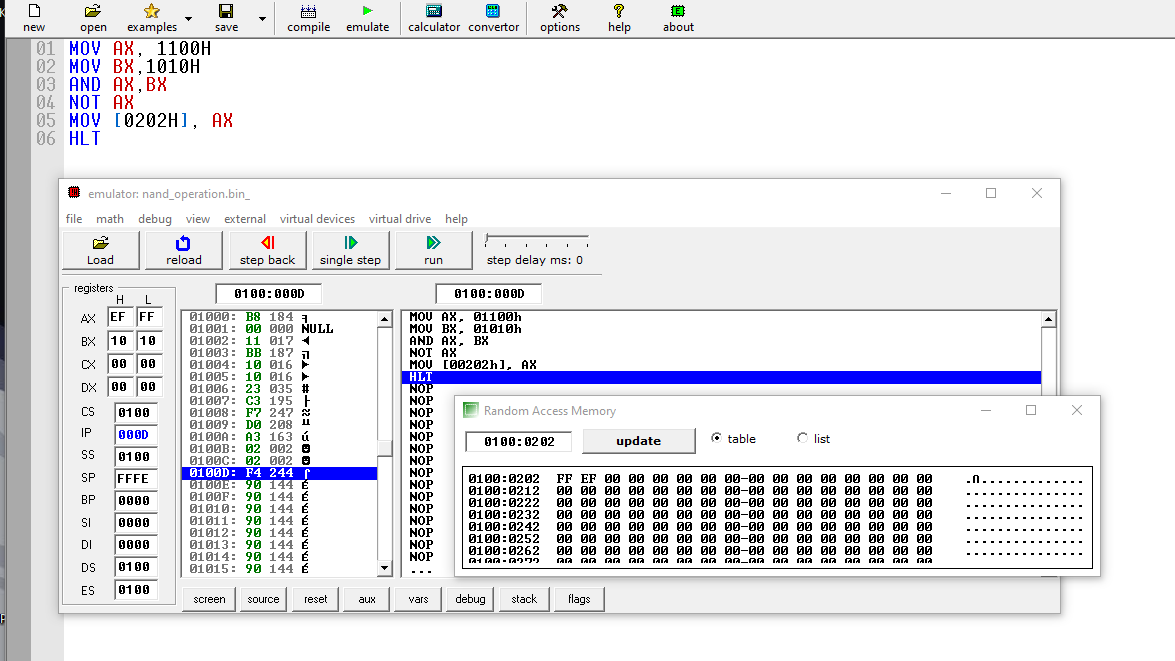
****

**OR Operation Output**

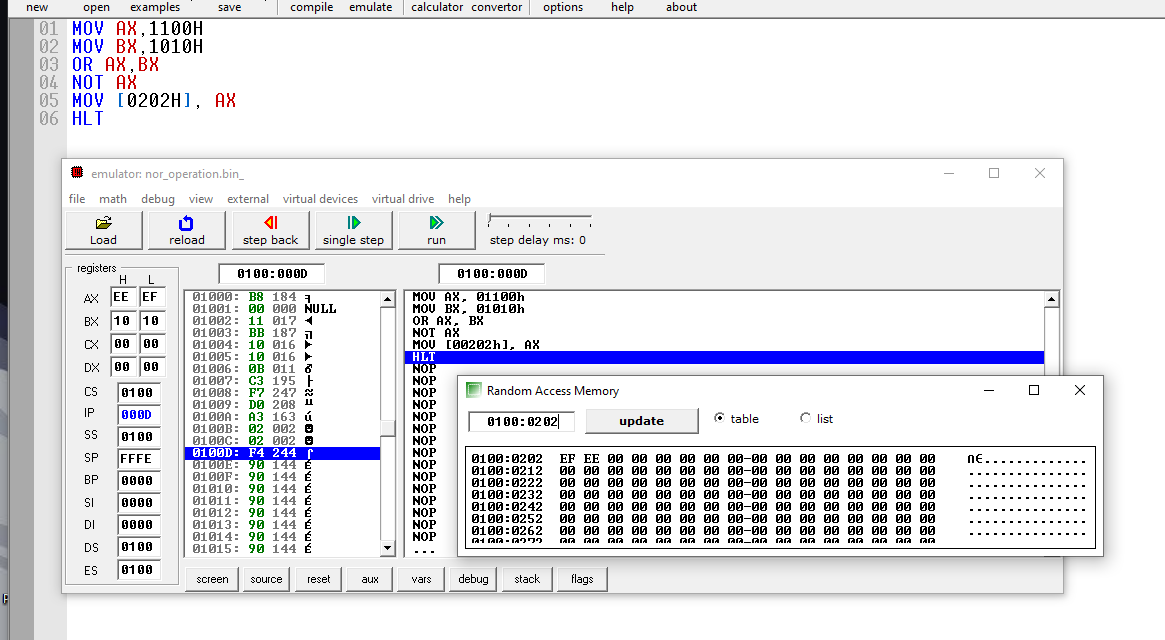
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**Post Lab**

**NAND Operation**

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**NOR Operation**

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